
Van der Waals ferroelectric heterostructures for in-memory computing and emergent electronics

Jean-Francois Dayen^{1,2}

¹ Université de Strasbourg, CNRS, Institut de Physique et Chimie des Matériaux de Strasbourg

² Institut Universitaire de France (IUF), 1 rue Descartes, 75231 Paris cedex 05, France.

E-mail: dayen@unistra.fr

2D ferroelectric materials are attracting fast growing interest for the implementation of complex more-than-Moore and beyond-Moore architectures that are challenging to design with standard thin film technology.¹ Here, I will present recent developments on the coupling of a 2D vdW electron gas with various ferroelectric gate controls. We will discuss how these systems allow for rethinking circuit topology and memory-logic interaction, opening up new research directions in the area of frugal computational enhancement and neuromorphic computing for AI.

I will first detail how by making use of the switchable polarization state of two splitted ferroelectric gates, the electrical potential landscape within a semiconductor channel can be permanently and **reconfigurably** modified.² While using the non-volatile ferroelectric states encoded in each gate, the **ferroelectric logic circuits** can function as six alternative logic gates, while CMOS circuit are limited to a single function. Such Re-FeFET circuits demonstrate high compactness, with an up to 80% reduction in transistor count compared to standard CMOS design.

Then, I will show the emulating of **synaptic plasticity** in vdW Ferroelectric Field Effect Transistor (FeFET) using unipolar or ambipolar 2D semiconductor.^{3,4} Combined electronic transport and piezo force microscopy investigations allows to carefully investigate the fine tuning of multidomains polarization landscape of the vdW ferroelectric gate, and its transduction into the conduction of the 2D semiconductor channel down to 50 nm scale for emulating artificial synapse plasticity. This dynamic synaptic reconfigurability offer new possibilities for next-generation neuromorphic computational architectures.

Finally, I will present how **light-structure interactions** in vdW systems allow for implementing the non-volatile electrical and optical control of the ferroelectric polarization in ferroelectric/semiconductor heterostructures.^{3,5,6} The wavelength-dependent study unveils ferroelectric polarization control and decouples the mechanisms driven by photogenerated carriers for each material and at the interfaces. Following, long-term potentiation/depression, and spike rate-dependent plasticity are shown using electrical AND optical controls, enabling **optically-stimulated and optically-assisted synaptic devices** implementation.

References

- [1] Jin, T. et al. *ACS Nano* 2022, 16, 9, 13595–13611.
- [2] A. Ram. et al. *ACS Nano* 2023, 17, 21, 21865–21877.
- [3] M. Soliman, et al., *ACS Appl. Mater. Interfaces* 2023, 15, 12, 15732.
- [4] A. Ram, et al., *ACS Appl. Mater. Interfaces* 2025, 17, 44, 60852–6086
- [5] K. Maity, et al. *ACS Appl. Mater. Interfaces* 2023, 15, 48, 55948.
- [6] K. Maity, et al., *ACS Appl. Electron. Mater.* 2026, doi/10.1021/acsaelm.5c02316